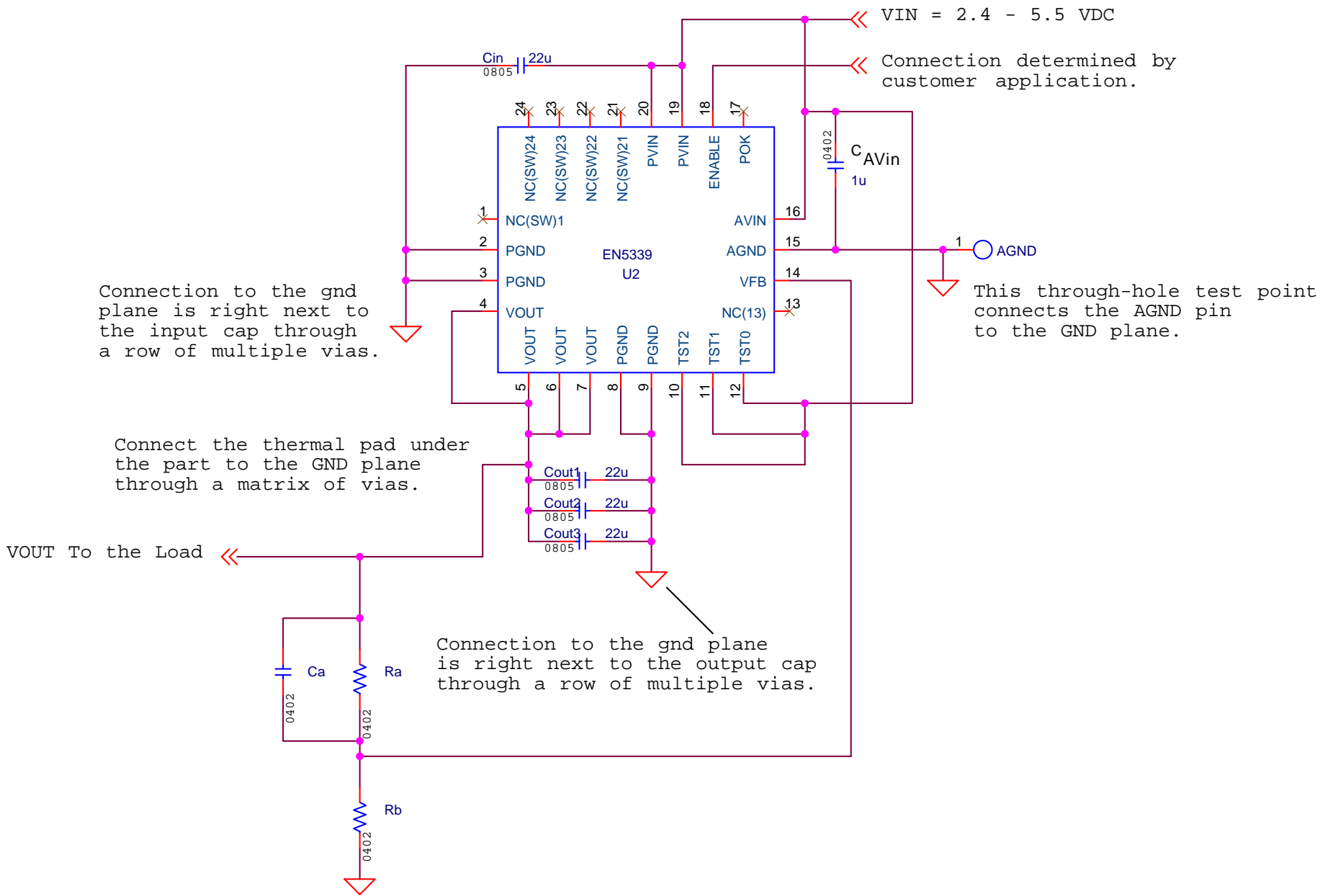This schematic only shows the critical components and traces for a minimum footprint layout. ENABLE, Vout-programming, and other small-signal pins need to be connected and routed according to the customer application.

VIN = 2.4 - 5.5 VDC

Connection determined by customer application.

Cin 22u
0805

C_{AVin} 1u
0402

EN5339
U2

24 NC(SW)24
23 NC(SW)23
22 NC(SW)22
21 NC(SW)21
20 PVIN
19 PVIN
18 ENABLE
17 POK

1 NC(SW)1
2 PGND
3 PGND
4 VOUT
5 VOUT
6 VOUT
7 VOUT
8 PGND
9 PGND
10 TST2
11 TST1
12 TST0

16 AVIN
15 AGND
14 VFB
13 NC(13)

1 AGND

Connection to the gnd plane is right next to the input cap through a row of multiple vias.

This through-hole test point connects the AGND pin to the GND plane.

Connect the thermal pad under the part to the GND plane through a matrix of vias.

Cout1 22u
0805
Cout2 22u
0805
Cout3 22u
0805

VOUT To the Load

Connection to the gnd plane is right next to the output cap through a row of multiple vias.

Ca 0402

Ra 0402

Rb 0402

SCH 06881
GBR 06882

CONFIDENTIAL

ENPIRION

Title: EN5339QI Critical Components Ckt for Minimum Footprint

Size	Drawing Number	Drawn By	Rev
		C. Romano	

Date: December 10, 2013

Sheet   of