

One-Stop Intel TXT Activation Guide



IBM® X-Series, iDataplex and BladeCenter Server Systems

Intel® Trusted Execution Technology (Intel® TXT) for Intel® Xeon processor-based servers is commonly used to enhance platform security by utilizing the underlying hardware based technology found in modern server platforms. Using a combination of the Intel Xeon processor-based and other industry leading platform technologies, such as Intel® Virtualization Technology (Intel VT), Trusted Platform Module (TPM), and appropriately configured BIOS with the Intel® SINIT ACM (authenticated code module); Intel TXT provides security against hypervisor, BIOS, firmware and other pre-launch software based attacks by establishing a 'root of trust' during the boot process. Enabling Intel TXT to protect your systems is a simple process and this will be showcased in this document.

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Assumptions & Guidance

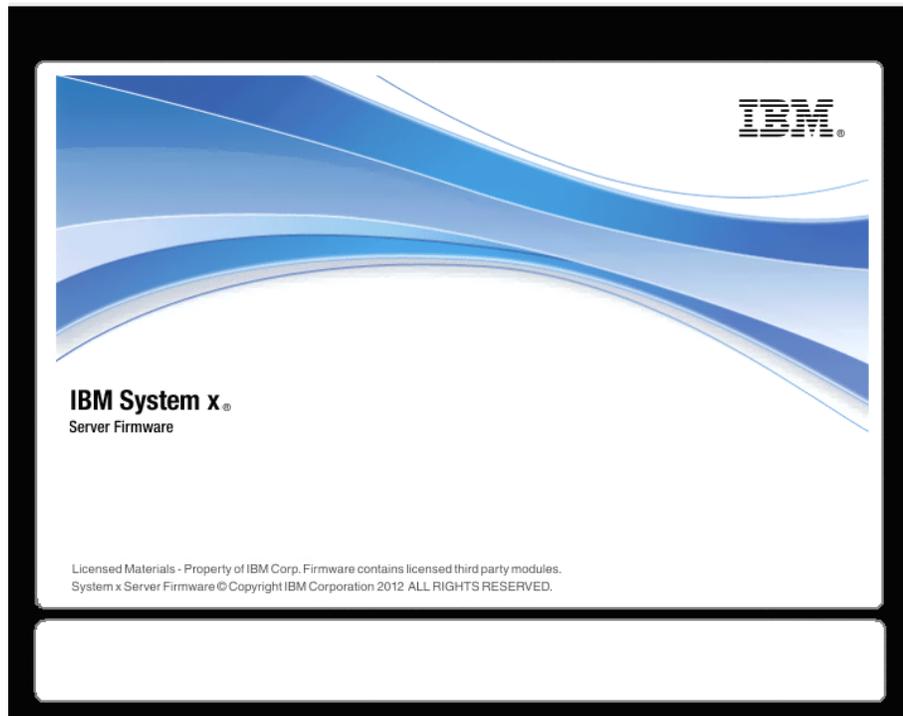
- This document is intended to provide guidance for activating the TPM/Intel TXT in BIOS/uEFI console.
- As available, this document is intended to provide guidance for scale activation of TPM/Intel TXT.
- This document requires fundamental systems engineering knowledge and is intended for Systems Engineers and Systems Administrators.
- This document covers step by step instructions for two IBM X-series server platforms based on the Intel Xeon processor E5-2600 V2 family. Many IBM platforms support Intel TXT with an embedded TPM v1.2 on the motherboard.
- Microsoft Windows Server software does not support trusted-boot scenarios that are supported by Intel TXT; use cases are based around Linux based server platforms which also includes VMWare ESXi and variations of Openstack cloud-server software.
- Trusted Boot (tboot) is an open source, pre- kernel/VMM module that uses Intel Trusted Execution Technology (Intel TXT) to perform a measured and verified launch of an OS kernel/VMM. Project details: <http://sourceforge.net/projects/tboot/>

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IBM Servers - X3650M4 and X3530M4

Note: Both of these IBM Server Platforms have very similar BIOS and configuration. There may be some slight differences between each model, but in general the operations that are shown for the X3650M4 will work on the X3530M4 model as well.



Platform Expectations

- IBM fully supports EFI and ships with native uEFI console
- IBM fully supports the TCG standard for Intel TXT/TPM activation which requires physical presence key
- IBM ships the TPM in enabled State by default.
- IBM ships the ACM as part of BIOS FW itself.

Out of the Box Configuration

1. By default IBM have TPM enabled
2. Press **F1** to enter to uEFI menu
3. Enter in to uEFI Menu > **System Setting** > **System Security** > **TXT State** > **Enable**
4. Save Settings and Press **Ctl+Alt+Del** to reboot the server.
5. *Ensure to check if TPM Status is Enabled/Activated and Intel TXT status is enabled. Press F1> System Setting > System Security*
6. **uEFI Menu** > **Boot Manager** > **Boot from device** > **Legacy Only** [*check/select*]; and select the boot device.. For ex: select CD ROM if user decides to install through CD medium

```

                                Boot Manager
-----
Primary Boot Sequence                                Boot system from a
Add Boot Option                                     specific device.
Delete Boot Option
Change Boot Order

Secondary (WOL) Boot Sequence
Add WOL Boot Option
Delete WOL Boot Option
Change WOL Boot Order

Boot Other
Boot From File
Boot From Device
Select Next One-Time Boot

                                     ↓

↑↓=Move Highlight      <Enter>=Select Entry      Esc=Exit
-----
```

```

                                Boot Devices Manager
-----

Legacy Only      [X]      DevicePath:
NIC1:PXE0 - Onboard Ethernet Port1      PciRoot (0x0) /Pci (0x2,0x2
NIC2:PXE1 - Onboard Ethernet Port2      ) /Pci (0x0,0x0) /Ctrl (0x0
NIC3:PXE2 - Onboard Ethernet Port3      ) /Scsi (0x0,0x0) /HD (1,GPT,
NIC4:PXE3 - Onboard Ethernet Port4      63AB198A-985B-43D7-BFAB-
DSA:Diagnosics - Diagnostics            8054AB386A65,0x40,0x1FC0
SAS:HD - Onboard SAS Controller          )

↑↓=Move Highlight      <Enter>=Select Entry      Esc=Exit
-----
```

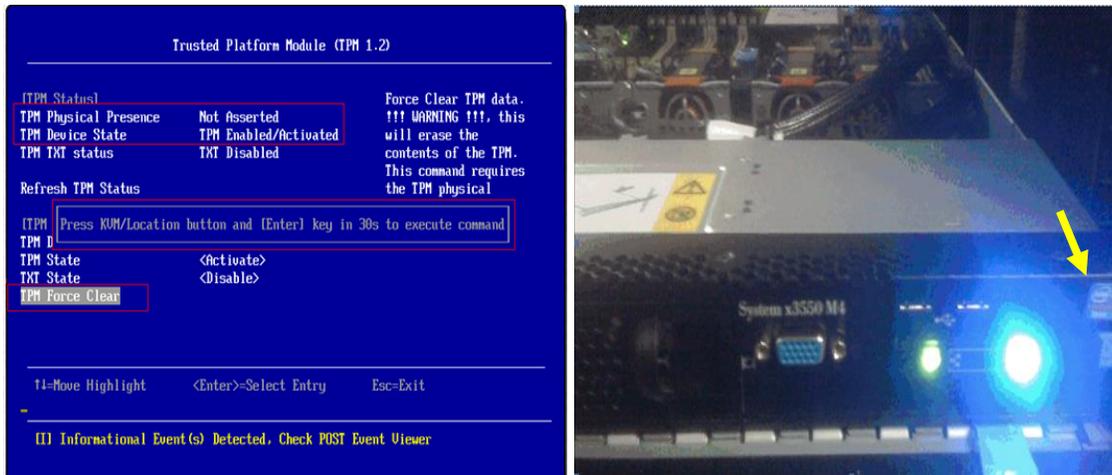
7. After Successful Installation, press **F1 > uEFI Menu > Boot Manager > Boot from device > legacy Only** [check/select] and select the boot device. For ex: On board SAS controller

TPM Clear and Reactivate Intel TXT/TPM

After TPM clear action, IBM servers require physical presence button press to reactivate the TPM/Intel TXT.

TPM clear can be done either in BIOS console or from OS using Trousers DLL. One of the requirements for TPM clear is to transfer the TPM ownership. TPM clear action will deactivate the TPM. Reboot is required to activate the TPM/Intel TXT again. Below are the steps to clear and reactivate the TPM/Intel TXT.

- Press F1 > Systems Setting > System Security > Select “TPM Force Clear”
- User will get the prompt to press the KVM/Location Button next to power button in front panel.
- Press the KVM/Location button and hit Enter in 30 seconds.



- User will get a Success message. **“Success! Reboot Required to enable this change”**
- Save the settings and reboot the server **Ctrl+Alt+Del**
- uEFI Menu > System Setting > **System Security > TXT State > Enable**
- **Ensure to check if TPM Status is Enabled/Activated and Intel TXT status is Enabled.**
- uEFI Menu > Save Settings and Press **Ctrl + del** to Reboot the server.
- **Press F1 > uEFI Menu > Boot Manager > Boot from device > Legacy Only** [check/select] ; and select the boot device.. For ex: select CD ROM if user decides to install through CD medium
- **After Successful Installation, press F1 > uEFI Menu > Boot Manager > Boot from device > legacy Only** [check/select] and select the boot device. For ex: On board SAS controller

Note:

- All IBM servers have TPM enabled by default but Intel TXT is disabled.
- To Enable Intel TXT, user doesn't need to press KVM button.
- KVM button Press action needs only when user clears the TPM [ON/OFF].
- TPM ON and TPM activate can be done together and so KVM press actions is needed only one time for both ON and Activate actions.

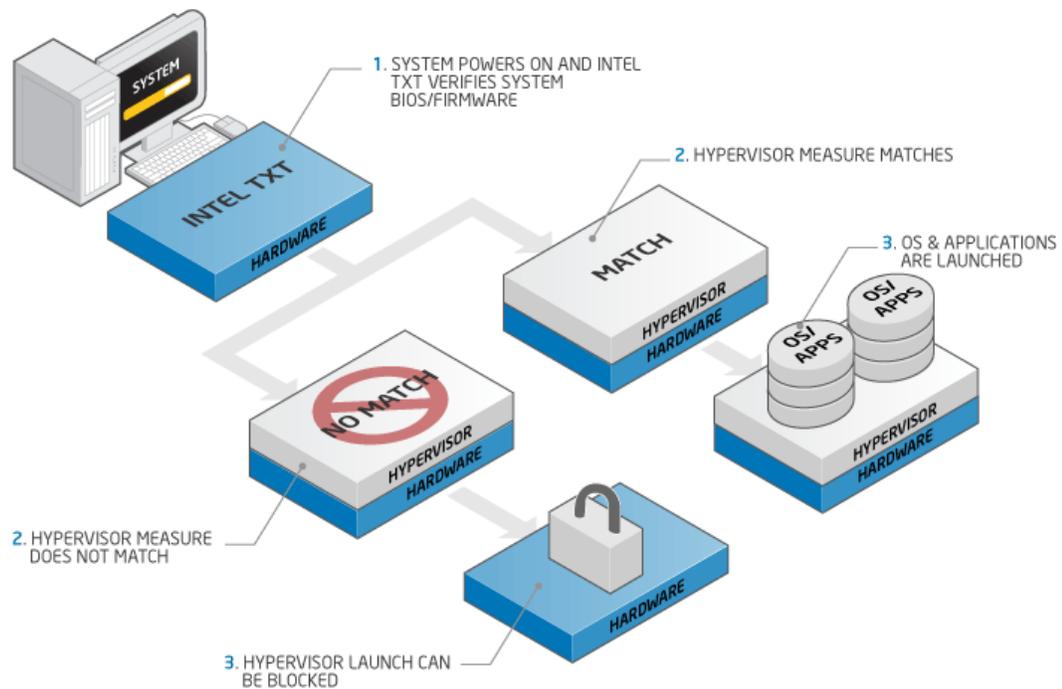
Scaling Activation of Intel TXT/TPM across Multiple Systems

Enabling Intel TXT/TPM on one system is great for testing and validating your platform. In real-world scenarios in the datacenter, customers generally have multiple systems that need enabling at the same time during setup and configuration. Fortunately many OEMs provide tools that lend themselves to assist the server administrator to perform this function.

Enabling Intel TXT across multiple systems allows for more use cases beyond the root-of-trust establishment on a single platform. Models such as Trusted Compute Pools can be developed where systems with Intel TXT can be placed on a 'whitelist' for access. This allows system administrators to place their highest security workloads on trusted platforms and reduce the threat to bare-metal attacks.

INTEL® TXT

INTEL TRUSTED EXECUTION TECHNOLOGY

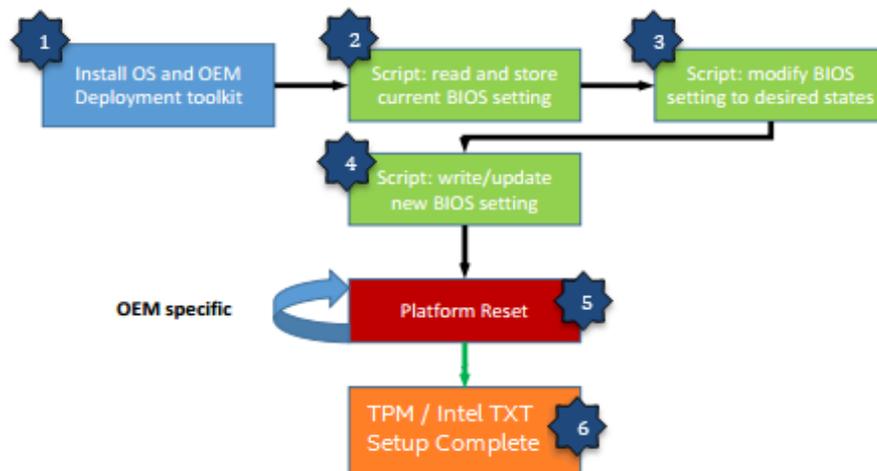


In order for Intel TXT to function properly the following dependencies need to be established:

- Intel Xeon processor based server platform with Intel TXT enabled BIOS
- Intel Virtualization Technology (Intel VT) must be enabled
- Intel Virtualization Technology with Directed I/O (Intel VT-d) must be enabled
- A Trusted Platform Module (TPM) v1.2 must be enabled and activated
- The platform specific [Intel SINIT ACM](#) needs to be installed into the platform
- Finally, you need a hypervisor that supports [trusted boot \(t-boot\)](#)

Bare Metal Provisioning of Intel TXT

The process to take a bare-metal system with unknown settings to a fully functional Intel TXT enabled platform can take a few minutes per system. The process can be run in-band from the OS, or out-of-band (OOB) via PXE or other remote process. The schematic below shows the high-level process of how a system is updated.



1. The Server PXE boots and installs the OS as well as the OEM deployment tool.
2. The setup and configuration script issues the command that reads the current BIOS setting of the server.
3. The setup and configuration script modifies the TPM/Intel TXT and other inter-related settings to the desired states as prescribed by the administrator.
4. The setup and configuration script issues command that writes and updates the BIOS setting then reboots the server.
5. After several reboots (OEM specific), the TPM/Intel TXT setting will take effect.
6. At this point, the server is automatically configured for TPM/Intel TXT support without accessing the BIOS manually.

Intel TXT Scale Provisioning for IBM Server Platforms

The [IBM Advanced Settings Utility](#) (ASU) can modify BIOS settings from the command line on multiple operating-system platforms. The ASU can perform the following tasks: modify the basic BIOS CMOS settings, Issue selected baseboard management controller setup commands, and issue selected Remote Supervisor Adapter and Remote Supervisor Adapter II ([IMM2](#)) setup commands.

The IBM ASU provides these benefits:

- Modify selected basic input/output system (BIOS) CMOS settings without the need to restart the system to access F1 settings
- Modify selected baseboard management controller setup settings
- Modify selected Remote Supervisor Adapter and Remote Supervisor Adapter II setup settings
- Secure boot configuration
- Mount ISO file or USB/CD/DVD to a remote IMM based system, IMM must be exposed to network and accessible

IBM ASU Installation

After downloading the IBM ASU, there are a few simple steps to deploy the ASU onto your system:

1. Open an xterm or other terminal window.
2. Go to the directory that contains the downloaded ASU files.
3. From a shell command prompt, type one of the following commands and press
 - If the .tgz file for ASU was downloaded: Enter **tar -zxvf filename.tgz** where filename is the name of the Advanced Settings Utility file for Linux that you downloaded. The files are extracted to the same directory.
 - If the .rpm file for ASU was downloaded: Enter **rpm -Uvh filename.rpm** where filename is the name of the Advanced Settings Utility file for Linux that you downloaded. The files are extracted to the */opt/ibm/toolscenter/asu* directory.

Here is an example of the configuration script that can be run on IBM Servers that will scan the BIOS, and change the appropriate settings that will set Intel TXT to be activated and ready for your hypervisor installation.

```
echo "read security current setting"
./asu64 show SystemSecurity --kcs
sleep 5

echo "read virtualization current setting"
./asu64 show Processor.IntelVirtualizationTechnology --kcs
sleep 5

echo "set virtualization setting"
./asu64 set Processor.IntelVirtualizationTechnology Enable --kcs
sleep 5

echo "set TXT setting"
./asu64 set SystemSecurity.TXTState Enable --kcs
sleep 5
```

How to check the Intel TXT/TPM status

Linux Distributions

Assumption:

- Users have successfully activated Intel TXT in BIOS and OS by following the respective guides.
- To Activate the Intel TXT in Linux OS users are requested to follow the Intel TXT OS Setup Guide.
- TPM Status Can be read from linux OS through TPM Device Driver in Dom0.
- Issue below command to find the status of the TPM

```
$ cat /sys/class/misc/tpm0/device/enabled
```

If it returns 0 then it is not enabled; if it returns 1 then it is enabled.

```
$ cat /sys/class/misc/tpm0/device/active
```

If it returns 0 then it is not active; if it returns 1 then it is active.

```
$ cat /sys/class/misc/tpm0/device/owned
```

If it returns 0 then it is not owned; if it returns 1 then it is owned.

\$ cat /sys/class/misc/tpm0/device/pcrs

Returns the PCR measurement values.

```
[root@XenTestbed ~]# cat /sys/class/misc/tpm0/device/pcrs
PCR-00: 83 DF FA 74 AB A6 23 9B E5 50 7C C7 8A 05 65 9F FE 6F 34 4D
PCR-01: 3A 3F 78 0F 11 A4 B4 99 69 FC AA 80 CD 6E 39 57 C3 3B 22 75
PCR-02: FE 87 F1 E2 23 F8 E7 36 6D 69 F4 03 35 AE B8 F4 74 00 07 F7
PCR-03: 3A 3F 78 0F 11 A4 B4 99 69 FC AA 80 CD 6E 39 57 C3 3B 22 75
PCR-04: C3 D9 B5 FE FD C2 35 89 45 ED E4 95 F8 D4 53 FF 7B 3C 1C 16
PCR-05: 70 58 97 12 22 AC D9 C2 40 76 D9 F1 3A 44 EF 6D 20 A9 87 07
PCR-06: 3A 3F 78 0F 11 A4 B4 99 69 FC AA 80 CD 6E 39 57 C3 3B 22 75
PCR-07: 3A 3F 78 0F 11 A4 B4 99 69 FC AA 80 CD 6E 39 57 C3 3B 22 75
PCR-08: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
PCR-09: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
PCR-10: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
PCR-11: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
PCR-12: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
PCR-13: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
PCR-14: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
PCR-15: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
PCR-16: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
PCR-17: 01 49 36 FB 8E 27 3D 53 82 36 36 23 5B 16 26 AB 25 F1 C5 14
PCR-18: D4 C0 79 EC C5 5B 8E 11 1A C9 6C E4 C3 E0 49 F8 00 1B DA E2
PCR-19: 31 27 1D ED 60 3D 7F F5 4F 29 2C AD E5 34 9E 3B 01 0C 3A 7E
PCR-20: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
PCR-21: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
PCR-22: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
PCR-23: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
[root@XenTestbed ~]#
```

"Txt-stat" Tool:

- **txt-stat** is the Intel TXT status tool that is part of Tboot kernel to get the status of Intel TXT measurement. **txt-stat** tool collects the information from RAM and displays.
- Users can use this tool to check if the Intel TXT launch/boot was successful or not.
- Ensure to run the **tcsd daemon** before running this tool.

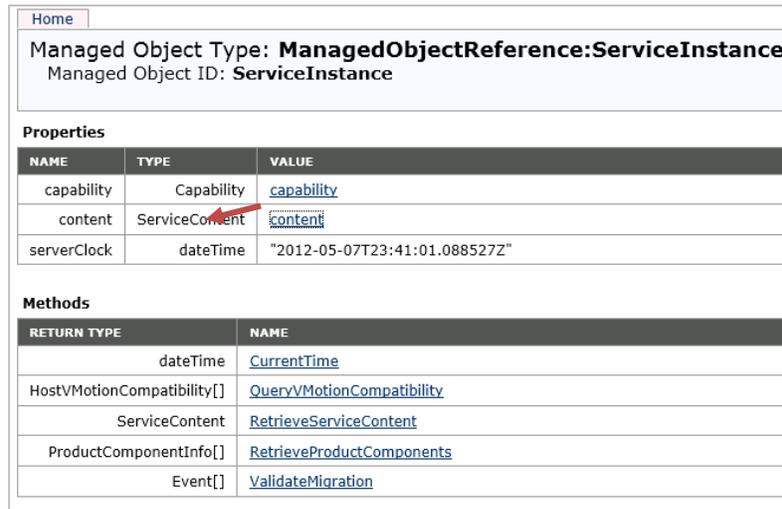
\$ tcsd

\$ txt-stat | more

```
Intel(r) TXT Configuration Registers:
STS: 0x00014081
  sender_done: TRUE
  sext_done: FALSE
  mem_config_lock: FALSE
  private_open: TRUE
  locality_1_open: FALSE
  locality_2_open: TRUE
ESTS: 0x00
  txt_reset: FALSE
E2STS: 0x0000000200000016
  secrets: TRUE
ERRORCODE: 0x00000000
DIDVID: 0x0000003fc0008086
  vendor_id: 0x8086
  device_id: 0xc000
  revision_id: 0x3f
FSBIF: 0x0000000000000000
QPIIF: 0x00000008c482000
SINIT.BASE: 0x8f700000
SINIT.SIZE: 131072B (0x20000)
HEAP.BASE: 0x8f720000
HEAP.SIZE: 917504B (0xe0000)
DPR: 0x00000008f800031
  lock: TRUE
  top: 0x8f900000
  size: 3MB (3145728B)
PUBLIC.KEY:
  08 77 7b 21 ec 4d 7f ce f7 68 2a 26 96 bc 5f 42
  a9 96 45 a4 21 81 10 7f 87 70 c2 24 37 fd e0 2c
.....
TXT measured launch: TRUE
secrets flag set: TRUE
.....
```

VMware ESXi 5.x

1. Install ESXi on the Intel TXT/TPM activated host and add to the vCenter.
2. Connect to the vCenter through IE browser <http://<vCenter IP Address>/mob>
3. Click on the “**add exceptions**” in the next screen
4. Enter the **credentials** of the vCenter to connect to ESXi hosts.
5. Click on “**Content**”



Home

Managed Object Type: **ManagedObjectReference:ServiceInstance**
Managed Object ID: **ServiceInstance**

Properties

NAME	TYPE	VALUE
capability	Capability	capability
content	ServiceContent	content
serverClock	dateTime	"2012-05-07T23:41:01.088527Z"

Methods

RETURN TYPE	NAME
dateTime	CurrentTime
HostVMotionCompatibility[]	QueryVMotionCompatibility
ServiceContent	RetrieveServiceContent
ProductComponentInfo[]	RetrieveProductComponents
Event[]	ValidateMigration

6. In the following screen, search for “**Rootfolder**” and click on the value “**group-d1**”
7. In the following screen, search for “**Childentity**” and click on the value “**Datacenter-2**”
8. In the following screen, search for “**Hostfolder**” and click on the value “**group-h4**”
9. In the following screen, search for “**Childentity**” and click on the value “**Domain-C7**”
10. In the following screen, search for “**Host**” and click on the value “**host <ip address>**”
11. In next screen drag down to Methods table and click on “**QueryTpmAttestationReport**”
12. A separate window will open up - Click on “**Invoke method**”
13. In the Next screen user can see the Platform Configuration Register (PCR) values populated.

Note:

- If ESXi host is not Intel TXT provisioned then you will not see any PCR values in step 13.
- If users are sure that TPM is provisioned correctly but TPM value is unset in v-center then as a work around, disconnects the host and reconnects the host if the TPM value is unset.

Troubleshooting Guide

1. How to determine if Intel TXT successfully launched?

In Linux Distributions:

Use txt-stat tool to check if the Intel TXT launch is successful.

```
root@ubuntu-tboot:~# txt-stat | grep measured
    TXT measured launch: TRUE
TBOOT: measured launch succeeded
```

In VMware ESXi:

You can verify if TPM is enabled on your ESXi hosts with the following command:

```
esxcli hardware trustedboot get
```

```
~ # esxcli hardware trustedboot get
    Drtm Enabled: true
    Tpm Present: true
```

If users see TPM value is unset though it is provisioned correctly, as a work around disconnect and reconnect the host in vCenter will usually resolve the issue.

2. How to validate the TPM:

There is tool called tpm-tools which is shipped with all Linux OS. This tools implements the TSS API and talks directly to the TPM

\$ tpm_selftest will show the current state of TPM

\$ tpm_version will show the tpm version

```
root@mwtstubx01h:~# tpm_version
TPM 1.2 Version Info:
Chip Version:      1.2.8.8
Spec Level:       2
Errata Revision:  2
TPM Vendor ID:    STM
TPM Version:      01010000
Manufacturer Info: 53544d20
```